



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In r	e A	qq	lica	tion	of:
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Tongbi Jiang

Serial No.: 09/543,034

Filed: April 5, 2000

For: TAPE ATTACHMENT CHIP-ON-BOARD ASSEMBLIES (as amended)

Confirmation No.: 6830

Examiner: D. Kang

Group Art Unit: 2811

Attorney Docket No.: 2269-3818.1US

(98-0887.01/US)

**Notice of Allowance Mailed:** 

December 5, 2005

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL995989057US

Date of Deposit with USPS: March 3, 2006

Person making Deposit: Timothy Palfreyman

## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In the Office Actions dated December 18, 2002 and August 11, 2003, the Examiner indicates:

Yew et al. fail to teach or suggest the width of adhesive tape extends beyond the edge of the semiconductor substrate opening and edge of the semiconductor die. It is desirable to improve the ability to visually inspect for proper attachment of the semiconductor chip to the semiconductor substrate.

Yew et al. fail to teach or suggest a composition of the first adhesive differs from a composition of the second adhesive. The use of differing adhesives compensates for the disparity in thermal expansion values typically existing between semiconductor substrate and semiconductor chip. Accordingly, one of ordinary skill in the art would not have been motivated to modify the teaching of Yew et al. to meet the claimed limitation.

Applicant concurs with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,

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